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Total Number of Pages in This Submission

20

Application Number 10/801789

Filing Date 03/16/04

First Named Inventor Andy T. Yu, et al

Art Unit 2824

Examiner Name Unassigned

Attorney Docket Number 49667.22371

ENCLOSURES (Check all that apply)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance Communication to TC
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	Carlton Fields, P. A.		
Signature			
Printed name	Li K. Wang		
Date	3/15/05	Reg. No.	44,393

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of:

Andy T. Yu, et al.

Serial No. 10/801,789

Filed: 3/16/04

Examiner: Unassigned

Group: 2824

**Title: Non-Volatile Electrically Alterable Memory Cell for Storing
Multiple Data and An Array Thereof**

INFORMATION DISCLOSURE STATEMENT

March 15, 2005

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

SIR:

Submitted herewith, on Forms PTO/SB/08A and PTO/SB/08B, are listings of documents known to applicant and/or his attorneys in compliance with the requirements of 37 CFR §1.56.

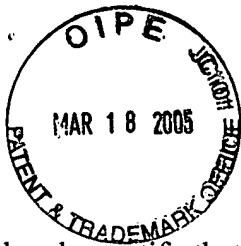
Consideration of the cited documents and making the same of record in the prosecution of the above-referenced application are respectfully requested.

Respectfully submitted,

CARLTON FIELDS, P. A.

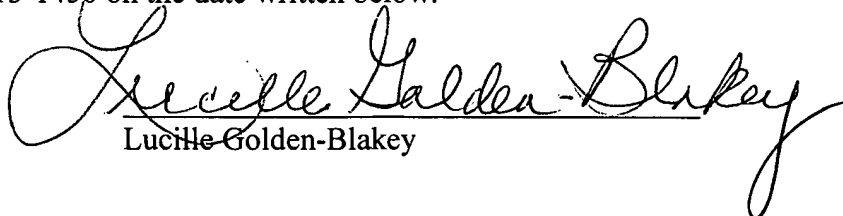
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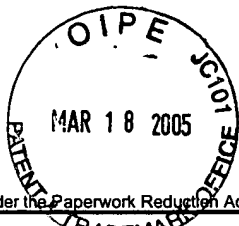


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Substitute for form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known

Application Number	11/801,789
Filing Date	03/16/04
First Named Inventor	Andy Yu, et al
Art Unit	2824
Examiner Name	Unassigned
Attorney Docket Number	49667.22371

Sheet 1 of 1

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		A Dual Gate Flash EEPROM Cell With Two-Bits Storage Capacity; M. Lorenzini, et al.; 1996 IEEE; 1996 Int'l. NonVolatile Memory Technology Conference	
		A Dual Gate Flash EEPROM Cell With Two-Bits Storage Capacity; M. Lorenzini, et al.; 1997 IEEE; Transactions on Components, Packaging and Manufacturing and Manufacturing Technology-Part A, Vol. 20, No. 2 June 1997	

Examiner
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